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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/741,670	12/18/2003	Sang-Hyun Oh	51876P456	4170	
8791 7	7590 06/28/2004		EXAMINER		
	OKOLOFF TAYLO	KESHAVAN, BELUR V			
12400 WILSHIRE BOULEVARD, SEVENTH FLOOR LOS ANGELES, CA 90025		ART UNIT	PAPER NUMBER		
	,		2825		

DATE MAILED: 06/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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. •	Application No.	Applicant(s)	
	10/741,670	OH,ET AL.	
Office Action Summary	Examiner	Art Unit	
	Belur V Keshavan	2825	
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the	correspondence ad	ldress
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be t ly within the statutory minimum of thirty (30) da will apply and will expire SIX (6) MONTHS fro e, cause the application to become ABANDON	timely filed ays will be considered timel m the mailing date of this c IED (35 U.S.C. § 133).	
Status			
 1) ☐ Responsive to communication(s) filed on 18 E 2a) ☐ This action is FINAL. 2b) ☐ This 3) ☐ Since this application is in condition for alloware closed in accordance with the practice under E 	s action is non-final. ince except for formal matters, p		e merits is
Disposition of Claims			
 4) Claim(s) 1-7 is/are pending in the application. 4a) Of the above claim(s) is/are withdra 5) Claim(s) is/are allowed. 6) Claim(s) 1 and 4 is/are rejected. 7) Claim(s) 2,3 and 5-7 is/are objected to. 8) Claim(s) are subject to restriction and/or 			
Application Papers			
9) ☐ The specification is objected to by the Examine 10) ☑ The drawing(s) filed on 18 December 2003 is/a Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) ☐ The oath or declaration is objected to by the Example 11.	are: a)⊠ accepted or b)⊡ object drawing(s) be held in abeyance. So stion is required if the drawing(s) is o	ee 37 CFR 1.85(a). objected to. See 37 C	FR 1.121(d).
Priority under 35 U.S.C. § 119			
a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority document 2. ☐ Certified copies of the priority document 3. ☐ Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Applica prity documents have been receiv uu (PCT Rule 17.2(a)).	ntion No ved in this National	Stage
·			
Attachment(s)		(DTO 110)	
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 	4)	Date	O-152)

Art Unit: 2825

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 and 4 are rejected under 35 U.S.C. 102(b) as being anticipated by applicant's admitted prior art, hereinafter APA, as shown in figure 3C.

Regarding claim 1, APA discloses a method for fabricating a ferroelectric random access memory device, comprising the steps of:

Forming a first inter-layer insulation layer (24) on a substrate (21); forming a storage node contact (25) connected with a partial portion of the substrate by passing through the first inter-layer insulation layer; forming a lower electrode (29) connected to the storage node contact on the first inter-layer insulation layer; forming a second inter-layer insulation layer (26) having a surface level lower than that of the lower electrode so that the second inter-layer insulation layer encompasses a bottom part of the lower electrode; forming an impurity diffusion barrier layer (27) encompassing an upper part of the lower electrode on the second inter-layer insulation layer; forming a ferroelectric layer (30) on the lower electrode and diffusion barrier layer; and forming a top electrode (31) on the ferroelectric layer.

Regarding claim 4, APA discloses a method wherein the step of forming the impurity diffusion barrier includes the steps of:

Depositing a second insulation layer (27) on an entire surface of a structure including the second inter-layer insulation layer (26); and performing an blanket etch-back process to the second insulation layer until a surface of the lower electrode (29) is exposed.

Allowable Subject Matter

Claims 2, 3, 5, 6 and 7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Belur V Keshavan whose telephone number is 571-272-1894. The examiner can normally be reached on 8-4:30 Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 10/741,670 Page 4

Art Unit: 2825

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Bvk. June 24, 2004.

Belur V. Keshavan. Examiner. Art Unit 2825.

MATTHEW SMITH SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800